

ABSTRACT OF THE DISCLOSURE

A data transmission line used connected continuously in a plurality of stages in an asynchronous system includes a transfer control circuit, synchronous and asynchronous data holding circuits and a timing adjustment circuit. The data transmission line receives and holds data transmitted from a data transmission line of a preceding stage or data output from an external clock synchronous circuit, and outputs and transmits the data to a data transmission line of a succeeding stage. Timing adjustment circuit adjusts data input timing by the transfer control circuit to the synchronous and asynchronous data holding circuits so that data can surely be taken in. It becomes possible to surely take in and transmit data output from external synchronous system in an asynchronous data transmission line at a desired arbitrary timing.